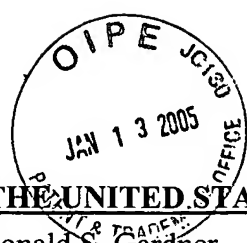


ITW



S/N 10/774,956

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Donald S. Gardner	Examiner:	Nema O. Berezny
Serial No.:	10/774,956	Group Art Unit:	2813
Filed:	February 9, 2004	Docket:	884.140US3
Title:	INDUCTORS FOR INTEGRATED CIRCUITS (as amended)		
Assignee:	Intel Corporation	Customer No:	21186

**RESPONSE TO RESTRICTION REQUIREMENT
AND PRELIMINARY AMENDMENT**

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed December 10, 2004, Applicant hereby elects, without traverse, Group I (claims 1-10) and cancels claims 11-35 without prejudice. Applicant reserves the right to reintroduce the non-elected claims 11-35 in one or more divisional applications at a later date.

For the record, Applicant respectfully disagrees with the Examiner's characterization of the subject matter of Groups I-III as drawn to "methods", as set forth in the Examiner's restriction requirement. Applicant respectfully asserts that the subject matter of original claims 1-35 is characterized, without limitation, by their corresponding preambles as follows:

<u>Claims</u>	<u>Subject Matter</u>
1-10, 31-35	integrated circuit
11-15, 22-27	integrated circuit package
16-21, 28-30	integrated circuit component